

WHAT IS CLAIMED IS:

1. A semiconductor integrated circuit driven by an external power, comprising:

5 a change unit whose state changes with lapse of time without the external power;

15 an output unit configured to output a signal in response to an instruction issued when the external power is supplied, the signal indicating a change of the state of the change unit; and

10 an execution unit configured to execute a process in response to the signal.

2. The semiconductor integrated circuit according to claim 1, wherein the output unit includes a plurality of output elements, each of the output 15 elements outputting the signal in response to the instruction.

3. The semiconductor integrated circuit according to claim 1, wherein the change unit includes a plurality of change elements, each of the change 20 elements whose state changes with lapse of time without the external power.

4. The semiconductor integrated circuit according to claim 3, wherein the output unit includes a plurality of output elements, each of the output 25 elements outputting the signal in response to the instruction.

5. The semiconductor integrated circuit according

to claim 1, further comprising a control unit configured to acquire time information based on the signal when the external power is supplied, and control the execution unit to make it execute the process using
5 the time information.

6. The semiconductor integrated circuit according to claim 5, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the
10 instruction.

7. The semiconductor integrated circuit according to claim 5, wherein the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without
15 the external power.

8. The semiconductor integrated circuit according to claim 7, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the
20 instruction.

9. The semiconductor integrated circuit according to claim 1, further comprising:
an antenna connection unit connected to an antenna and configured to acquire a current induced in the
25 antenna by electromagnetic induction; and

a power supply connected to the antenna connection unit, the power supply acquiring the current,

rectifying and smoothing a acquired current, and supplying a rectified and smoothed current to an interior of the semiconductor integrated circuit.

10. The semiconductor integrated circuit according
5 to claim 9, wherein

the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without the external power, and

10 the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction.

11. The semiconductor integrated circuit according to claim 9, further comprising:

15 a control unit configured to control the execution unit to make it execute the process based on the signal;

20 a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna connection unit; and

a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

25 12. The semiconductor integrated circuit according to claim 5, further comprising:

an antenna connection unit connected to an antenna

and configured to acquire a current induced in the antenna by electromagnetic induction; and

a power supply connected to the antenna connection unit, the power supply acquiring the current, rectifying and smoothing a acquired current, and supplying a rectified and smoothed current to an interior of the semiconductor integrated circuit.

13. The semiconductor integrated circuit according to claim 12, wherein

10 the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without the external power, and

15 the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction issued when the external power is supplied.

14. The semiconductor integrated circuit according to claim 12, further comprising:

20 a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna connection unit; and

25 a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

15. A semiconductor integrated circuit module

comprising:

a semiconductor integrated unit including:

5 a power supply which acquires a current from an antenna, and rectifies and smoothes a acquired current, and supplying, as a power, a rectified and smoothed current to an interior of the semiconductor integrated unit;

a change unit whose state changes with lapse of time without the power;

10 an output unit configured to output a signal in response to an instruction issued when the power supply supplies the power, the signal indicating a change of the state of the change unit; and

15 an execution unit configured to execute a process in response to the signal;

a sealing material which seals the semiconductor integrated unit; and

20 an antenna terminal which connects the power supply to the antenna, the antenna terminal being exposed on an outer surface of the sealing material.

16. The semiconductor integrated circuit module according to claim 15, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction when the power is supplied.

25 17. The semiconductor integrated circuit module according to claim 15, wherein the semiconductor

integrated unit further includes:

a control unit configured to control the execution unit to make it execute the process based on the signal;

5 a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna terminal; and

10 a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

18. The semiconductor integrated circuit module according to claim 15, further comprising a control unit configured to acquire time information based on 15 the signal, and control the execution unit to make it execute the process using the time information.

19. The semiconductor integrated circuit module according to claim 18, wherein the output unit includes a plurality of output elements, each of the output 20 elements outputting the signal in response to the instruction when the power is supplied.

20. The semiconductor integrated circuit module according to claim 18, wherein the semiconductor integrated unit further includes:

25 a control unit configured to control the execution unit to make it execute the process based on the signal;

a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna terminal; and

5 a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

21. An information apparatus comprising:

10 an antenna which acquires a current induced by electromagnetic induction;

a power supply connected to the antenna, the power supply acquiring a current, rectifying and smoothing the acquired current, and supplying, as a power, a rectified and smoothed current to an interior of 15 the information apparatus;

a change unit whose state changes with lapse of time without the power from the power supply;

20 an output unit configured to output a signal in response to an instruction issued when the power supply supplies the power, the signal indicating a change of the state of the change unit; and

an execution unit configured to execute a process in response to the signal.

22. The information apparatus according to 25 claim 21, wherein

the change unit includes a plurality of change elements, each of the change elements whose state

changes with lapse of time without the power, and
the output unit includes a plurality of output
elements, each of the output elements outputting the
signal in response to the instruction when the power is
5 supplied.

23. The information apparatus according to
claim 21, further comprising:

a control unit configured to control the execution
unit to make it execute the process based on the
10 signal;

a demodulation unit configured to demodulate
a command superposed on the acquired current and output
the command to the control unit, the demodulation unit
being connected to the antenna; and

15 a modulation unit configured to modulate a result
of the process and output a modulated result to the
antenna.

24. The information apparatus according to
claim 21, further comprising a control unit configured
20 to acquire time information based on the signal, and
control the execution unit to make it execute the
process using the time information.

25. The information apparatus according to
claim 24, wherein

25 the change unit includes a plurality of change
elements, each of the change elements whose state
changes with lapse of time without the power, and

the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction issued when the power is supplied.

5 26. The information apparatus according to claim 24, further comprising:

 a control unit configured to control the execution unit to make it execute the process based on the signal;

10 a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna; and

15 a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.